

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

1-4. (cancelled)

5. (new) An apparatus for providing an adjustable bandwidth high pass filter, the apparatus comprising:

a high pass filter having an input capacity in series with a resistive ladder having a plurality of resistances coupled in series, said coupling between the capacity and the first resistor of the resistive ladder defining a first tap and successive couplings between resistances forming successive taps, the last resistance of said resistive ladder being coupled to a ground;

a plurality of bandwidth adjusting resistances, each first side of the bandwidth adjusting resistance coupled to the first tap; and

a plurality of switches that provide the coupling of the second side of each of the bandwidth adjusting resistances to said ground.

6. (new) The apparatus as in claim 5, wherein said ground comprises an AC ground, said AC ground providing a DC bias.

7. (new) The apparatus as in claim 5, wherein the switches further comprise semiconductor switches.

8. (new) The apparatus as in claim 7, wherein the semiconductor devices further comprise MOSFETS (Metal Oxide Semiconductor Field Effect Transistors).

9. (new) The apparatus as in claim 8, wherein the MOSFET is a N-type MOSFET.

10. (new) The apparatus as in claim 8, wherein the MOSFET is a P-type MOSFET.

11. (new) The apparatus as in claim 5, wherein the apparatus further comprises:

a MOSFET device having a source, a gate and a drain, the source coupled to the second side of one of said bandwidth adjusting resistors;

the drain of the MOSFET device coupled to said ground and said gate being coupled to a control circuit.

12. (new) The apparatus as in claim 11, wherein the control circuit comprises:
an amplifier output coupled to the gate of the switching device.

13. (new) The apparatus as in claim 12, wherein the amplifier further comprises:
a tristate buffer amplifier; and
a pull-up resistance coupled between the output of the tristate and a power supply.

14. (new) The apparatus as in claim 13, wherein the power supply is the power supply for an integrated circuit containing the apparatus.

15. (new) The apparatus as in claim 13, wherein the pull-up resistance comprises a long channel triode device.

16. (new) The apparatus as in claim 11, further comprising a capacity disposed between the gate and source of the MOSFET switch.

17. (new) The apparatus as in claim 15, wherein the drain of the long channel triode device is coupled to a power supply voltage, the gate to the ground of the power supply voltage and the source is coupled to the gate of the MOSFET switch.